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L5	1	703/18.ccls. and @pd>"20070801"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/10/17 16:44
L8	178	power and (noise adj analysis) and plane\$1 and @ad<"20030601"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/10/17 16:52
L9	78	power and (noise near analysis) and (printed adj circuit) and @ad<"20030601"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/10/17 16:53



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[Hybrid electromagnetic modeling of noise interactions in packagedelectronics based on the partial- ... - all 5 versions »](#)

W Pinello, AC Cangellaris, A Ruehli - Microwave Theory and Techniques, IEEE Transactions on, 1997 - [ieeexplore.ieee.org](#)

... interconnect delay, reflection, and simultaneous **switching noise**. ... interconnects on **printed circuit** boards, which ... a rigorous electromagnetic **PEEC** model, while ...

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[Wave model solution to the ground/power plane noise problem - all 3 versions »](#)

GT Lei, RW Techentin, PR Hayes, DJ Schwab, BK ... - Instrumentation and Measurement, IEEE Transactions on, 1995 - [ieeexplore.ieee.org](#)

... known as delta-I or **switching noise**, appears as ... The **PEEC** techniques yield equivalent circuits in terms of ... x 7.5 in) double- sided **printed circuit** board, with ...

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[Analysis of power/ground-plane EMI decoupling performance using thepartial-element equivalent ... - all 4 versions »](#)

B Archambeault, AE Ruehli - Electromagnetic Compatibility, IEEE Transactions on, 2001 - [ieeexplore.ieee.org](#)

... and TA Schreyer, "Computation of **switching noise** in **printed circuit** boards," IEEE ... for the partial element equivalent circuit (**PEEC**) method," presented ...

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[Modeling and simulation of core \*\*switching noise\*\* for ASICs - all 4 versions »](#)

N Na, J Choi, M Swaminathan, JR Libous... - IEEE Transactions on Advanced Packaging, 2002 - [ece.gatech.edu](#)

... CBGA) package on a **printed circuit** board (PCB ... the role of plane resonances on core **switching noise**. ... partial element equivalent circuit (**PEEC**) based approaches ...

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[Estimating the noise mitigation effect of local decoupling in \*\*printed circuit boards\*\* - all 5 versions »](#)

J Fan, W Cui, JL Drewniak, TP Van Doren, JL ... - Advanced Packaging, IEEE Transactions on [see also ..., 2002 - [ieeexplore.ieee.org](#)

... HIGH-FREQUENCY noise in **printed circuit** boards (PCBs ... referred to as simultaneous **switching noise** (SSN) and ... partial element equivalent circuit (**PEEC**) [10], [11 ...

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[An efficient simultaneous \*\*switching noise\*\* analysis of high densitymulti-layer packages and PCBs ...](#)

JH Choi, YS Hong, CW Ko, YG Kim, TS Kim, JT Kong - VLSI and CAD, 1999. ICVC'99. 6th International Conference on, 1999 - [ieeexplore.ieee.org](#)

... 10 hours using the conventional **PEEC** approach. ... Speed Digital Packages and **Printed Circuit Boards**, 35th ... SH Lee, "A Simultaneous **Switching Noise** Analysis System ...

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[T Hubing](#)

**Computation of switching noise in printed circuit boards**

JG Yook, V Chandramouli, LPB Katehi, KA Sakallah, ... - Components, Packaging, and Manufacturing Technology, Part A, ..., 1997 - [ieeexplore.ieee.org](#)

... 20, NO. 1, MARCH 1997 Computation of **Switching Noise** in **Printed Circuit Boards**

Jong-Gwan Yook, Student Member, IEEE, V. Chandramouli, Student Member, IEEE, ...

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**Power bus decoupling on multilayer printed circuit boards - all 3 versions »**

TH Hubing, JL Drewniak, TP Van Doren, DM Hockanson - Electromagnetic Compatibility, IEEE Transactions on, 1995 - [ieeexplore.ieee.org](#)

... 19, A simple finite-difference frequency-domain (FDFD) algorithm for analysis of **switching noise** in **printed circuit boards** and packages, Ramahi, OM; Subramanian ...

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**A novel power plane with integrated simultaneous switching noise mitigation capability using high ... - all 10 versions »**

T Kamgaing, OM Ramahi - Microwave and Wireless Components Letters, IEEE [see also ..., 2003 - [ieeexplore.ieee.org](#)

... In **printed circuit boards** (PCB), each digital gate is usually connected between two ... 1. The simultaneous **switching noise** (SSN) becomes acute when the noise ...

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**Reducing simultaneous switching noise and EMI on ground/powerplanes by dissipative edge termination - all 7 versions »**

I Novak, SM Inc, MA Burlington - Advanced Packaging, IEEE Transactions on [see also ..., 1999 - [ieeexplore.ieee.org](#)

... [17] P. Wetmore, F. Chua, and X. Yuan, "Modeling simultaneous **switching noise** in high speed multilayer packages and **printed circuit boards**," in Proc. ...

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**Modeling of simultaneous switching noise in high speed systems - all 6 versions »**

S Chun, M Swaminathan, LD Smith, J Srinivasan, Z ... - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - [ieeexplore.ieee.org](#)

... CHUN et al.: MODELING OF SIMULTANEOUS **SWITCHING NOISE** IN HIGH SPEED SYSTEMS ... been

used to model a system containing packages on a **printed circuit board**, demon ...

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**Interconnect and circuit modeling techniques for full-chip powersupply noise analysis - all 2 versions »**

HH Chen, JS Neely - Components, Packaging, and Manufacturing Technology, Part B: ..., 1998 - [ieeexplore.ieee.org](#)

... and achieve the desired resolution for a full-chip **switching noise** analysis, we use ... and the bottom of the package is attached to the **printed circuit board** (PCB ...

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 Na, N.; Swaminathan, M.; Libous, J.; O'Connor, D.;  
Electronic Components and Technology Conference, 2001. Proceedings., 51st  
 29 May-1 June 2001 Page(s):1095 - 1101  
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 Ruehli, A.E.; Cangellaris, A.C.;  
Proceedings of the IEEE  
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 Jun Fan; Hao Shi; Orlandi, A.; Knighten, J.L.; Drewniak, J.L.;  
Advanced Packaging, IEEE Transactions on [see also Components, Packaging and Manufacturing  
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- ☐ **5. Modeling and simulation of core switching noise for ASICs**  
 Nanju Na; Jinwoo Choi; Swaminathan, M.; Libous, J.R.; O'Connor, D.P.;  
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- ☐ 1. A simple finite-difference frequency-domain (FDFD) algorithm for analysis of switching noise boards and packages  
 Ramahi, O.M.; Subramanian, V.; Archambeault, B.;  
[Advanced Packaging, IEEE Transactions on \[see also Components, Packaging and Manufacturing Advanced Packaging, IEEE Transactions on\]](#)  
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- ☐ 3. Computation of switching noise in printed circuit boards  
 Yook, J.-G.; Chandramouli, V.; Katehi, L.P.B.; Sakallah, K.A.; Arabi, T.R.; Schreyer, T.A.;  
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- ☐ 5. Capacitor placement using genetic algorithms to reduce switching noise  
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